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Mano. M.D. Ciletti,
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1.27 For a deck with
52 cards, we need 6
bits ($32 < 52 < 64$).
Let the msb's select
the suit (e.g.,
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encoded respectively
as 00, 01, 10, and 11.
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$$1.100102 =$$

$$0001.10012 = 1.916$$
$$= 1 + 9/16 = 1.56310$$

$$(b) 110.0102 =$$

$$0110.01002 = 6.416$$
$$= 6 + 4/16 = 6.2510$$

1.11 Reason:

110.0102 is the same as 1.100102 shifted to the left by two

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context, this book concentrates on modern and evolving knowledge and design skills.

Hardware description language (HDL)-based design and verification is emphasized--Verilog examples are used extensively throughout. By treating digital logic

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today, and how it is used to build high-performance alternatives to software. Offers a fresh, up-to-date approach to digital design, whereas most literature available is sorely outdated. Progresses though low levels of design, making a clear distinction between

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in the book to real-
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Edition covers the
fundamentals of
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and reinforces logic
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Microprocessor
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Combining an engaging and humorous writing style with an updated and hands-on approach to digital design, this book takes the reader from the fundamentals of digital logic to the actual design of an ARM processor. By the end of this book,

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readers will be able to build their own microprocessor and will have a top-to-bottom understanding of how it works. Beginning with digital logic gates and progressing to the design of combinational and sequential circuits, this book uses these fundamental building

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blocks as the basis for designing an ARM processor.

SystemVerilog and VHDL are integrated throughout the text in examples illustrating the methods and techniques for CAD-based circuit design. The companion website includes a chapter on I/O systems with

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Practical examples that show how to use the Raspberry Pi computer to communicate with peripheral devices such as LCDs, Bluetooth radios, and motors. This book will be a valuable resource for students taking a course that combines digital logic and computer

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architecture or
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Covers the
fundamentals of
digital logic design
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concepts through the
design of an ARM
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examples of the two most prominent Hardware Description Languages (HDLs)—SystemVerilog and VHDL—which illustrate and compare the ways each can be used in the design of digital systems. Includes examples throughout the text that enhance the reader's

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Understanding and retention of key concepts and techniques. The Companion website includes a chapter on I/O systems with practical examples that show how to use the Raspberry Pi computer to communicate with peripheral devices such as LCDs,

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Bluetooth radios, and
motors. The
Companion website
also includes
appendices covering
practical digital
design issues and C
programming as well
as links to CAD tools,
lecture slides,
laboratory projects,
and solutions to
exercises.

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This book presents the basic concepts used in the design and analysis of digital systems and introduces the principles of digital computer organization and design.

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DIGITAL SYSTEMS DESIGN USING

VERILOG integrates coverage of logic design principles, Verilog as a hardware design language, and FPGA implementation to help electrical and computer engineering students master the process of designing and testing new

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configurations. A
Verilog equivalent of
authors Roth and
John's previous
successful text using
VHDL, this practical
book presents Verilog
constructs side-by-
side with hardware,
encouraging students
to think in terms of
desired hardware
while writing

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synthesizable Verilog.
Following a review of
the basic concepts of
logic design, the
authors introduce the
basics of Verilog
using simple
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examples, followed by
models for simple
sequential circuits.
Subsequent chapters
ask readers to tackle
more and more

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