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HiPC is an international conference on all aspects of high performance computing. It serves as a forum to present current work by researchers from around the world as well as highlight activities in Asia in the high performance computing area.

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High Performance Computing - HiPC 2007 Book Subtitle 14th International Conference, Goa, India, December 18-21, 2007, Proceedings Editors. Srinivas Aluru; Manish Parashar; Ramamurthy Badrinath; Viktor K. Prasanna; Series Title Theoretical Computer Science and General Issues Series Volume 4873 Copyright 2007 Publisher Springer-Verlag Berlin Heidelberg Copyright Holder

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This book constitutes the refereed proceedings of the 14th International Conference on High-Performance Computing, HiPC 2007, held in Goa, India, in December 2007. The 53 revised full papers presented together with the abstracts of 5 keynote talks were carefully reviewed and selected from 253 submissions.

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The 14th annual IEEE International Conference on High Performance Computing (HiPC 2007) will be held in Goa, India, during December 18-21, 2007. It will serve as a forum to present the current work by researchers from around the world, and act as a venue to provide stimulating discussions and highlight high performance computing activities in Asia.

HiPC - International Conference on High Performance Computing
High Performance Computing - HiPC 2007, 14th International Conference, Goa, India, December 18-21, 2007, Proceedings. Lecture Notes in Computer Science 4873, Springer 2007 , ISBN 978-3-540-77219-4 Keynote Addresses (Abstracts)

dblp: HiPC 2007
HiPC 2007 will focus on the design and analysis of high performance computing and networking systems and their scientific, engineering, and commercial applications. In addition to technical sessions consisting of contributed papers, the conference will include invited presentations, a poster session, tutorials, and vendor presentations.

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HiPC 2020 is the 27th edition of the IEEE International Conference on High Performance Computing, Data, and Analytics. The conference focus is HPC as well as Data Science. The meeting will be organized under two focus areas: High Performance Computing (HPC) and Scalable Data Science.

HiPC - High Performance Computing
The 14th IEEE International Conference on High Performance Computing (HiPC 2007), to be held in Goa, India, during December 18-21, 2007, will serve as a forum for presenting current work by researchers from around the world as well as to highlight activities in Asia, in the area of high performance computing.

HiPC - International Conference on High Performance Computing
This book constitutes the refereed proceedings of the 14th International Conference on High-Performance Computing, HiPC 2007, held in Goa, India, in December 2007. The 53 revised full papers presented together with the abstracts of 5 keynote talks were carefully reviewed and selected from 253 submissions.

High performance computing--HiPC 2007 : 14th international ...
27 th ieee international conference on high performance computing, data, & analytics IMPORTANT UPDATE: Due to the COVID-19 pandemic the conference will be organized virtually retaining the same dates i.e. 16th-17th December, 2020.

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High Performance Computing (HiPC 2007) Goa, India, December 18-21, 2007 HiPC 2007 Conference Program Program-at-a-Glance Tuesday, December 18, 2007. 8:30am - 6:30pm Workshops . 8:30am - 6:30pm Tutorials I & II Wednesday, December 19, 2007. 8:00am - 8:15am. Inauguration and Opening Remarks 8:15am - 8:30 am Introduction by . Ashwini Kumar Nanda. Title:

HiPC06 Detailed Program
Harish P., Narayanan P.J. (2007) Accelerating Large Graph Algorithms on the GPU Using CUDA. In: Aluru S., Parashar M., Badrinath R., Prasanna V.K. (eds) High Performance Computing - HiPC 2007. HiPC 2007. Lecture Notes in Computer Science, vol 4873. Springer, Berlin, Heidelberg. https://doi.org/10.1007/978-3-540-77220-0_21

This book constitutes the refereed proceedings of the 14th International Conference on High-Performance Computing, HiPC 2007, held in Goa, India, in December 2007. The 53 revised full papers presented together with the abstracts of five keynote talks were carefully reviewed and selected from 253 submissions. The papers are organized in topical sections on a broad range of applications including I/O and FPGAs, and microarchitecture and multiprocessor architecture.

This book constitutes the refereed proceedings of the 15th International Conference on High-Performance Computing, HiPC 2008, held in Bangalore, India, in December 2008. The 46 revised full papers presented together with the abstracts of 5 keynote talks were carefully reviewed and selected from 317 submissions. The papers are organized in topical sections on applications performance optimizazion, parallel algorithms and applications, scheduling and resource management, sensor networks, energy-aware computing, distributed algorithms, communication networks as well as architecture.

This tutorial book presents six carefully revised lectures given at the Spring School on Datatype-Generic Programming, SSDGP 2006. This was held in Nottingham, UK, in April 2006. It was colocated with the Symposium on Trends in Functional Programming (TFP 2006), and the Conference of the Types Project (TYPES 2006). All the lectures have been subjected to thorough internal review by the editors and contributors, supported by independent external reviews.

This book constitutes the refereed proceedings of the 13th International Conference on High-Performance Computing, HiPC 2006, held in Bangalore, India in December 2006. The 52 revised full papers presented together with the abstracts of 7 invited talks were carefully reviewed and selected from 335 submissions. The papers are organized in topical sections on scheduling and load balancing, architectures, network and distributed algorithms, application software, network services, applications, ad-hoc networks, systems software, sensor networks and performance evaluation, as well as routing and data management algorithms.

Annotation. This book constitutes the refereed proceedings of the 11th International Conference on High-Performance Computing, HiPC 2004, held in Bangalore, India in December 2004. The 48 revised full papers presented were carefully reviewed and selected from 253 submissions. The papers are organized in topical sections on wireless network management, compilers and runtime systems, high performance scientific applications, peer-to-peer and storage systems, high performance processors and routers, grids and storage systems, energy-aware and high-performance networking, and distributed algorithms.

"This book presents, discusses, shares ideas, results and experiences on the recent important advances and future challenges on enabling technologies for achieving higher performance"--Provided by publisher.

Dark Silicon and the Future of On-chip Systems, Volume 110, the latest release in the Advances in Computers series published since 1960, presents detailed coverage of innovations in computer hardware, software, theory, design and applications, with this release focusing on an Introduction to dark silicon and future processors, a Revisiting of processor allocation and application mapping in future CMPs in the dark silicon era, Multi-objectivism in the dark silicon age, Dark silicon aware resource management for many-core systems, Dynamic power management for dark silicon multi-core processors, Topology specialization for networks-on-chip in the dark silicon era, and Emerging SRAM-based FPGA architectures. Provides in-depth surveys and tutorials on new computer technology Covers well-known authors and researchers in the field Presents extensive bibliographies with most chapters Includes volumes that are devoted to single themes or subfields of computer science, with this release focusing on Dark Silicon and Future On-chip Systems

This book gathers a selection of peer-reviewed papers presented at the Tiangong-2 Data Utilization Conference, which was held in Beijing, China, in December 2018. As the first space laboratory in China, Tiangong-2 carries 3 new types of remote sensing payloads - the Wide-band Imaging Spectrometer (WIS), Three-dimensional Imaging Microwave Altimeter (TIMA), and Multi-band Ultraviolet Edge Imaging Spectrometer (MUEIS) - for observing the Earth. The spectrum of the WIS covers 18 bands, from visible to thermal infrared, with a swath of 300km. The TIMA is the first-ever system to use interferometric imaging radar altimeter (InIRA) technology to measure sea surface height and land topography at near-nadir angles with a wide swath. In turn, the MUEIS is the world's first large-field atmospheric detector capable of quasi-synchronously detecting the characteristics of ultraviolet limb radiation in the middle atmosphere. The Earth observation data obtained by Tiangong-2 has attracted many research groups and been applied in such diverse areas as land resources, water resources, climate change, environmental monitoring, agriculture, forestry, ecology, oceanography, meteorology and so on. The main subjects considered in this proceedings volume include: payload design, data processing, data service and application. It also provides a comprehensive introduction to the research results gleaned by engineers, researchers and scientists throughout the lifecycle of the Tiangong-2 Earth observation data, which will improve the payload development and enhance remote sensing data applications.

Energy Science and Applied Technology includes contributions on a wide range of topics:- Technologies in geology, mining, oil and gas exploration and exploitation of deposits- Energy transfer and conversion, materials and chemical technologies- Environmental engineering and sustainable development- Electrical and electronic technology, power system

This book provides basic and fundamental knowledge of various aspects of energy-aware computing at the component, software, and system level. It provides a broad range of topics dealing with power-, energy-, and temperature-related research areas for individuals from industry and academia.